

UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT NO. : US 6,993,571 B2

DATED : JANUARY 31, 2006

INVENTOR : MICHAEL DAVID KISTLER

ASSIGNEE : INTERNATIONAL BUSINESS MACHINES CORPORATION

**REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 37 CFR 1.322**

ATTENTION: CERTIFICATE OF CORRECTIONS BRANCH
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

This paper is submitted in connection with the above referenced patent. The Patent contains one or more errors and, accordingly, a Certificate of Correction is respectfully requested. Attachment A to this document shows the error(s) and requested corrections. In addition, a proposed Certificate of Correction is attached.

Assignee believes that, because the errors corrected herein were incurred through the fault of the Patent and Trademark Office, no fee is required under the provisions of 37 CFR 1.322. If, however, a fee is required, the Commissioner is authorized to charge any such fee to Lally & Lally, L.L.P. Deposit Account No. 50-0335/IBM.5246.

If any questions arise during the processing of this request, please do not hesitate to contact the undersigned at the number listed below.

Respectfully submitted,



Joseph P. Lally
Reg. No. 38,947
ATTORNEY FOR ASSIGNEE

PLEASE FORWARD THE CERTIFICATE
TO ASSIGNEE AT:

IBM CORPORATION, IP LAW DEPT.
11400 Burnet Road
Austin, Texas 78758

JUNE 22, 2006

ATTACHMENT A

Claim 1:

1. A method of operating a server cluster including a set of server devices each connected to a local area network, comprising:

preventing access to a selected server's memory by other servers on the server cluster when the selected server is powered up;

deactivating the selected server responsive to a decrease in server cluster traffic;

responsive to deactivating the selected server, permitting the other servers on the cluster to access at least a portion of the selected ~~server's~~ server's memory; and

responsive to a request received by one of the other servers for a file stored in the selected server's file cache, retrieving the file from the selected server's file cache over the local area network, wherein deactivating the selected server includes transitioning the selected server's processors to a low power state while maintaining power to the selected server's NIC, peripheral bus and system memory.

Claim 8:

8. A server device suitable for use in a data processing network comprising:

at least one processor;

a system memory accessible to the processor via a system bus;

a network interface card (NIC), including a NIC controller and memory, connected to the system bus and providing a connection to the local area network;

server code means for deactivating the processor;

NIC code means for preventing access to the server device's memory by other servers on the server cluster when the server device is activated; and

NIC code means for enabling the other servers to retrieve a file from the system memory of the server device when the server device is deactivated, wherein deactivating the server device includes transitioning the server device's processors to a low power state while maintaining power to the server device's NIC, peripheral bus and system memory.

Claim 13:

13. A data processing network including a set of interconnected server devices, each server device comprising:

at least one processor;

a system memory accessible to the processor via a system bus;

a network interface card (NIC), including a NIC controller and memory, connected to the system bus and providing a connection to the local area network;

server code means for deactivating the processor;

NIC code means for preventing access to the server device's memory by other servers on the server cluster when the server device is activated; and

NIC code means for enabling the other servers to retrieve a file from the system memory of the server device when the server device is deactivated, wherein deactivating the server device includes transitioning the server device's processors to a low power state while maintaining power to the server device's NIC, peripheral bus and system memory.

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,993,571 B2

APPLICATION NO.: 09/931,290

ISSUE DATE : JANUARY 31, 2006

INVENTOR(S) : MICHAEL DAVID KISTLER

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claims:

1. A method of operating a server cluster including a set of server devices each connected to a local area network, comprising:

preventing access to a selected server's memory by other servers on the server cluster when the selected server is powered up;

deactivating the selected server responsive to a decrease in server cluster traffic;

responsive to deactivating the selected server, permitting the other servers on the cluster to access at least a portion of the selected server's server's memory; and

responsive to a request received by one of the other servers for a file stored in the selected server's file cache, retrieving the file from the selected server's file cache over the local area network, wherein deactivating the selected server includes transitioning the selected server's processors to a low power state while maintaining power to the selected server's NIC, peripheral bus and system memory.

8. A server device suitable for use in a data processing network comprising:

at least one processor;

a system memory accessible to the processor via a system bus;

MAILING ADDRESS OF SENDER (Please do not use customer number below):

International Business Machines Corporation
Intellectual Property Law Department - 4054
11400 Burnet Road, Building 3
Austin, Texas 78758

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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Page 2 of 3

PATENT NO. : 6,993,571 B2

APPLICATION NO.: 09/931,290

ISSUE DATE : JANUARY 31, 2006

INVENTOR(S) : MICHAEL DAVID KISTLER

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

a network interface card (NIC), including a NIC controller and memory, connected to the system bus and providing a connection to the local area network;

server code means for deactivating the processor;

NIC code means for preventing access to the server device's memory by other servers on the server cluster when the server device is activated; and

NIC code means for enabling the other servers to retrieve a file from the system memory of the server device when the server device is deactivated, wherein deactivating the server device includes transitioning the server device's processors to a low power state while maintaining power to the server device's NIC, peripheral bus and system memory.

13. A data processing network including a set of interconnected server devices, each server device comprising:

at least one processor;

a system memory accessible to the processor via a system bus;

a network interface card (NIC), including a NIC controller and memory, connected to the system bus and providing a connection to the local area network;

server code means for deactivating the processor;

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NIC code means for preventing access to the server device's memory by other servers on the server cluster when the server device is activated; and

NIC code means for enabling the other servers to retrieve a file from the system memory of the server device when the server device is deactivated, wherein deactivating the server device includes transitioning the server device's processors to a low power state while maintaining power to the server device's NIC, peripheral bus and system memory.

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